



UNITED STATES DEPARTMENT OF COMMERCE
Patent and Trademark Office

Address COMMISSIONER OF PATENTS AND TRADEMARKS
Washington, D. C. 20231

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.
-----------------	-------------	----------------------	---------------------

SB

EXAMINER

ART UNIT	PAPER NUMBER
----------	--------------

DATE MAILED:

9

Please find below and/or attached an Office communication concerning this application or proceeding.

Commissioner of Patents and Trademarks

Advisory Action

Application No.

09/405,945

Applicant(s)

JIN ET AL.

Examiner

Lynette T. Umez-Eronini

Art Unit

1765

--The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

THE REPLY FILED 16 October 2001 FAILS TO PLACE THIS APPLICATION IN CONDITION FOR ALLOWANCE. Therefore, further action by the applicant is required to avoid abandonment of this application. A proper reply to a final rejection under 37 CFR 1.113 may only be either: (1) a timely filed amendment which places the application in condition for allowance; (2) a timely filed Notice of Appeal (with appeal fee); or (3) a timely filed Request for Continued Examination (RCE) in compliance with 37 CFR 1.114.

PERIOD FOR REPLY [check either a) or b)]

- a) ☐ The period for reply expires _____ months from the mailing date of the final rejection.
- b) ☒ The period for reply expires on: (1) the mailing date of this Advisory Action, or (2) the date set forth in the final rejection, whichever is later. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of the final rejection. **ONLY CHECK THIS BOX WHEN THE FIRST REPLY WAS FILED WITHIN TWO MONTHS OF THE FINAL REJECTION. See MPEP 706.07(f).**

Extensions of time may be obtained under 37 CFR 1.136(a). The date on which the petition under 37 CFR 1.136(a) and the appropriate extension fee have been filed is the date for purposes of determining the period of extension and the corresponding amount of the fee. The appropriate extension fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the shortened statutory period for reply originally set in the final Office action; or (2) as set forth in (b) above, if checked. Any reply received by the Office later than three months after the mailing date of the final rejection, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

1. ☐ A Notice of Appeal was filed on _____. Appellant's Brief must be filed within the period set forth in 37 CFR 1.192(a), or any extension thereof (37 CFR 1.191(d)), to avoid dismissal of the appeal.
2. ☐ The proposed amendment(s) will not be entered because:
- (a) ☐ they raise new issues that would require further consideration and/or search (see NOTE below);
 - (b) ☐ they raise the issue of new matter (see Note below);
 - (c) ☐ they are not deemed to place the application in better form for appeal by materially reducing or simplifying the issues for appeal; and/or
 - (d) ☐ they present additional claims without canceling a corresponding number of finally rejected claims.

NOTE: _____

3. ☐ Applicant's reply has overcome the following rejection(s): _____.
4. ☐ Newly proposed or amended claim(s) _____ would be allowable if submitted in a separate, timely filed amendment canceling the non-allowable claim(s).
5. ☒ The a) ☐ affidavit, b) ☐ exhibit, or c) ☒ request for reconsideration has been considered but does NOT place the application in condition for allowance because: See Continuation Sheet.
6. ☐ The affidavit or exhibit will NOT be considered because it is not directed SOLELY to issues which were newly raised by the Examiner in the final rejection.
7. ☐ For purposes of Appeal, the proposed amendment(s) a) ☐ will not be entered or b) ☐ will be entered and an explanation of how the new or amended claims would be rejected is provided below or appended.

The status of the claim(s) is (or will be) as follows:

Claim(s) allowed: none.Claim(s) objected to: none.Claim(s) rejected: 1-11, 13-14, and 16-19.Claim(s) withdrawn from consideration: none.

8. ☐ The proposed drawing correction filed on _____ is a) ☐ approved or b) ☐ disapproved by the Examiner.
9. ☐ Note the attached Information Disclosure Statement(s) (PTO-1449) Paper No(s). _____
10. ☐ Other: _____

Continuation of 5. does NOT place the application in condition for allowance because: Applicants argue lack of motivation to combine the transistor of Chang ('740), which lacks a insulating or dielectric layer that is formed on the top gate, with the SAC process of Hsue ('654).

In response to applicant's argument that there is no suggestion to combine the references, the examiner recognizes that obviousness can only be established by combining or modifying the teachings of the prior art to produce the claimed invention where there is some teaching, suggestion, or motivation to do so found either in the references themselves or in the knowledge generally available to one of ordinary skill in the art. See *In re Fine*, 837 F.2d 1071, 5 USPQ2d 1596 (Fed. Cir. 1988) and *In re Jones*, 958 F.2d 347, 21 USPQ2d 1941 (Fed. Cir. 1992). In this case, the reason to combine the teachings of Hsue and Chang is found on page 3 in Office Action of Paper No. 7. However, Chang is relied upon to teach the deficiency of Hsue, which is a transistor having a gate length of less than 0.2 microns, as recited in claim 1. Chang teaches using a gate electrode (same as a transistor gate) of 0.1 micrometer in length to make up a transistor (column 4, lines 55 and 56). Hence, Chang's gate electrode is transistor gate that has a gate length less than 0.2 microns.

Applicants argue the references fail to show or suggest a first insulating layer of doped silicon dioxide, in claims 12 and 18. Applicants' arguments are unpersuasive. It is well known in the art that a insulation or dielectric layer may comprise of materials such as doped or undoped silicon dioxide. Substitution of one for the other is seen as equivalent because they are materials that can overlie semiconductive materials. Substitution of one for the other would have been obvious for the purpose of forming a contact region in a semiconductor substrate.

Applicants argue the references fail to teach the etch selectivities as recited in claim 12. Applicants arguments are unpersuasive. It would have been obvious to one having ordinary skill in the art at the time of the claimed invention to employ any of a variety of processing variables such as those claimed by the applicant. They are well-known variables in the etching art and known to affect both the rate and quality of the etching process. The selection of a particular value would be optimized by conducting routine experimentation for the purpose of obtaining the best-etched product. Changes in temperature, concentrations, or other process conditions of an old process do not impart patentability unless the recited ranges are critical, i.e., they produce a new and unexpected result. *In re Aller et al.*, 105 USPQ 233.

